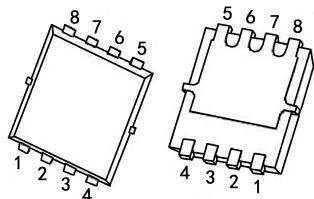


-40V P-Channel Mosfet

FEATURES

- $R_{DS(ON)} \leq 9\text{m}\Omega$ (7 $\text{m}\Omega$ Typ.) @ $V_{GS}=-10\text{V}$
- $R_{DS(ON)} \leq 13.5\text{m}\Omega$ (9 $\text{m}\Omega$ Typ.) @ $V_{GS}=-4.5\text{V}$
- AEC Q101 qualified
- Green Product (RoHS compliant)

PDFN5*6-8L



APPLICATIONS

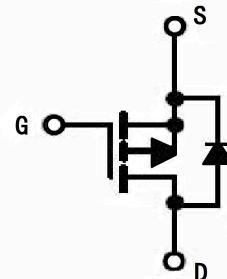
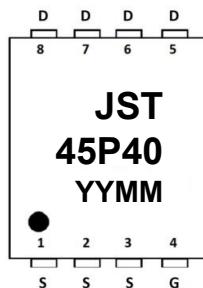
- Car Seat
- PWM Applications
- Load Switch
- Power Management

1: S 3: S 5: D 7: D

2: S 4: G 6: D 8: D

P-CHANNEL MOSFET

MARKING



MAXIMUM RATINGS ($T_c=25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Max.	Units
V_{DSS}	Drain-Source Voltage	-40	V
V_{GSS}	Gate-Source Voltage	± 20	V
I_D	Continuous Drain Current @ $V_{GS}=10\text{V}$ note1	$T_c = 25^\circ\text{C}$	A
		$T_c = 100^\circ\text{C}$	A
I_{DM}	Pulsed Drain Current note2	-180	A
P_D	Power Dissipation	33	W
$R_{\theta JC}$	Thermal Resistance, Junction to Case	4.5	$^\circ\text{C}/\text{W}$
T_J, T_{STG}	Operating and Storage Temperature Range	-55 to +175	$^\circ\text{C}$

MOSFET ELECTRICAL CHARACTERISTICS $T_c=25^\circ\text{C}$ unless otherwise specified

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units
Off Characteristic						
$V_{(\text{BR})\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{GS}=0\text{V}$, $I_D = -250\mu\text{A}$	-40	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = -40\text{V}$, $V_{GS}=0\text{V}$	-	-	-1	μA
I_{GSS}	Gate to Body Leakage Current	$V_{DS}=0\text{V}$, $V_{GS} = \pm 20\text{V}$	-	-	± 100	nA
On Characteristics						
$V_{GS(\text{th})}$	Gate Threshold Voltage	$V_{DS}=V_{GS}$, $I_D = -250\mu\text{A}$	-1.0	-1.5	-2.5	V
$R_{DS(\text{on})}$ note3	Static Drain-Source on-Resistance	$V_{GS} = -10\text{V}$, $I_D = -20\text{A}$	-	7	9	$\text{m}\Omega$
		$V_{GS} = -4.5\text{V}$, $I_D = -15\text{A}$	-	9	13.5	
Dynamic Characteristics note4						
C_{iss}	Input Capacitance	$V_{DS} = -20\text{V}$, $V_{GS}=0\text{V}$, $f=1.0\text{MHz}$	-	4940	-	pF
C_{oss}	Output Capacitance		-	427	-	pF
C_{rss}	Reverse Transfer Capacitance		-	375	-	pF
Q_g	Total Gate Charge	$V_{DS} = -20\text{V}$, $I_D = -20\text{A}$, $V_{GS} = -10\text{V}$	-	54	-	nC
Q_{gs}	Gate-Source Charge		-	9.5	-	nC
Q_{gd}	Gate-Drain("Miller") Charge		-	11	-	nC
Switching Characteristics note4						
$t_{d(on)}$	Turn-on Delay Time	$V_{DD} = -20\text{V}$, $I_D = -20\text{A}$, $V_{GS} = -10\text{V}$, $R_{\text{GEN}} = 2.5\Omega$	-	13	-	ns
t_r	Turn-on Rise Time		-	27	-	ns
$t_{d(off)}$	Turn-off Delay Time		-	68	-	ns
t_f	Turn-off Fall Time		-	37	-	ns
Drain-Source Diode Characteristics and Maximum Ratings						
V_{SD}	Drain to Source Diode Forward Voltage	$V_{GS}=0\text{V}$, $I_s = -20\text{A}$	-	-	-1.2	V

Notes:1. $T_c=25^\circ\text{C}$ Limited only by maximum temperature allowed. Calculated continuous current based on

maximum allowable junction temperature.

2. PW $\leq 10\mu\text{s}$, Duty cycle $\leq 1\%$
3. Pulse Test: Pulse Width $\leq 300\mu\text{s}$, Duty Cycle $\leq 2\%$
4. Guaranteed by design, not subject to production testing

TYPICAL CHARACTERISTICS

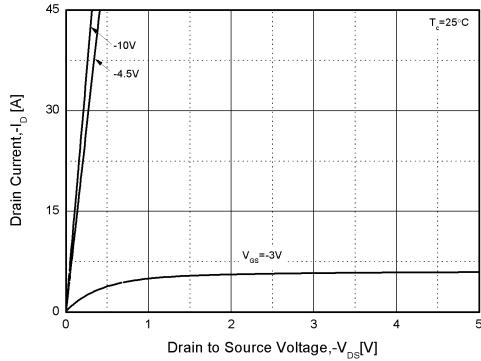


Figure1. Output Characteristics

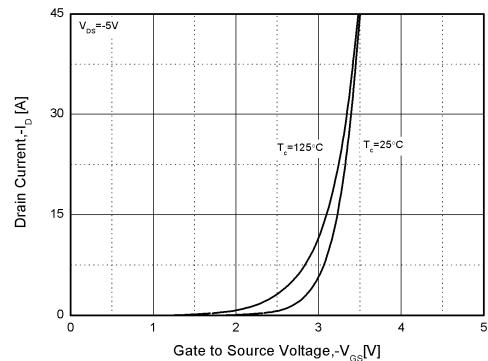


Figure2. Transfer Characteristics

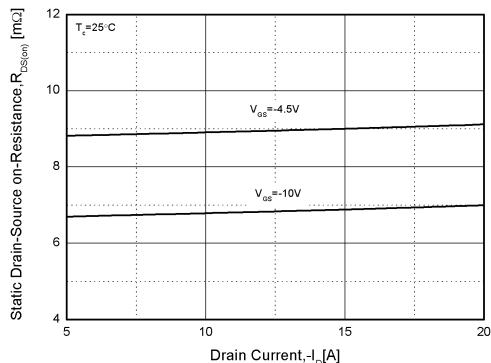


Figure3. Rdson-Drain Current

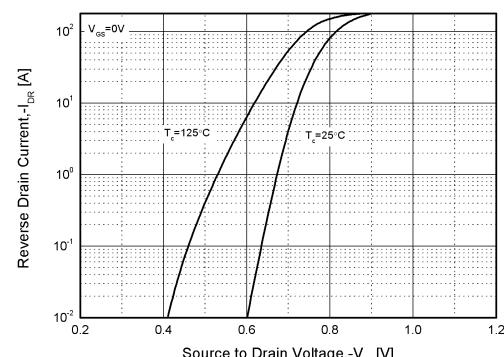


Figure4. Typical Source-Drain Diode Forward Voltage

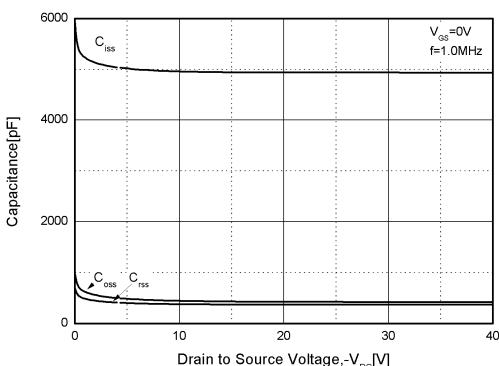


Figure5. Capacitance Characteristics

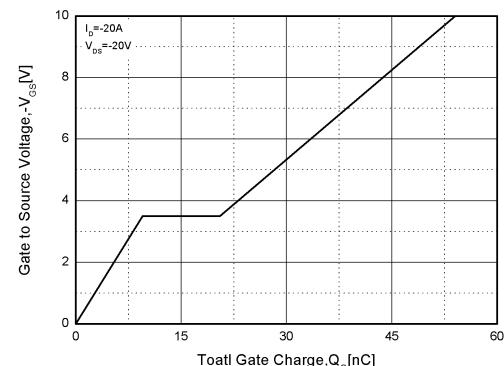


Figure6. Gate Charge

TYPICAL CHARACTERISTICS (cont.)

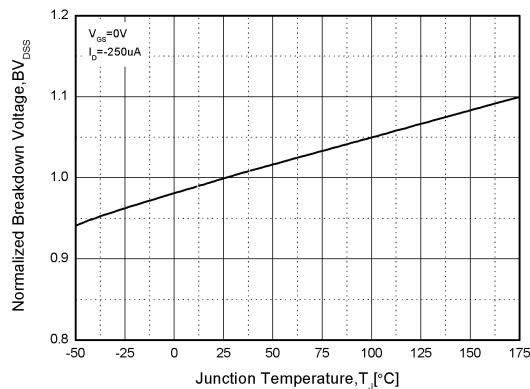


Figure 7. Normalized Breakdown Voltage vs. Temperature

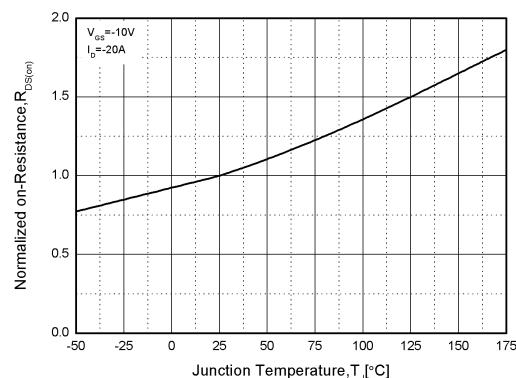
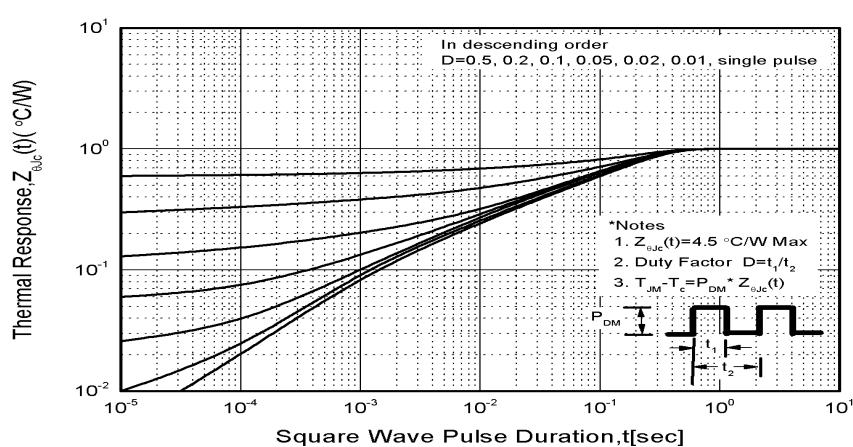
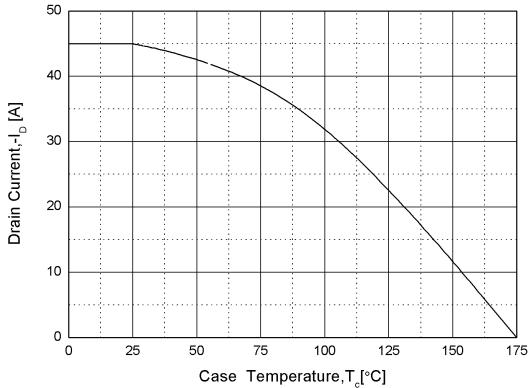
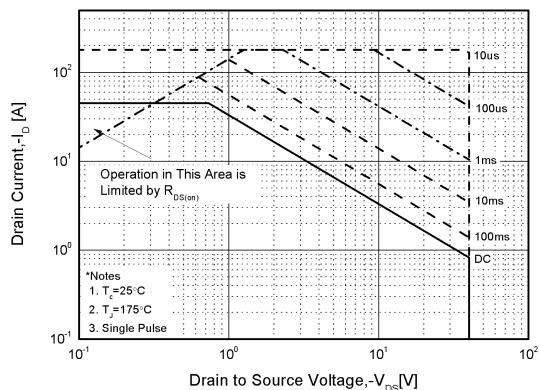
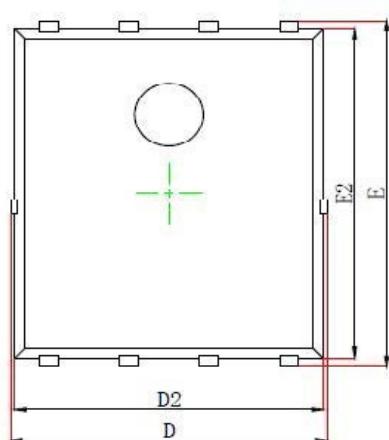


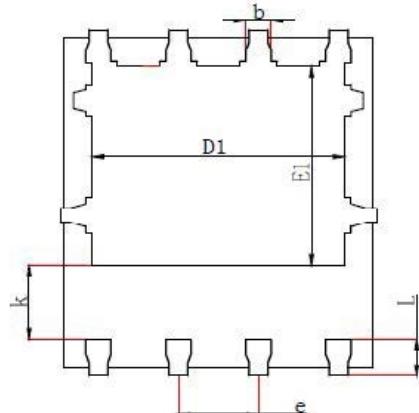
Figure 8. Normalized on-Resistance vs. Temperature



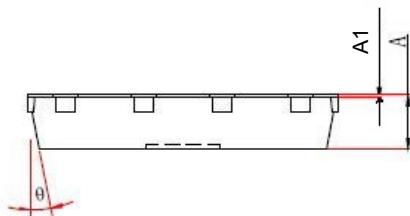
PDFN5*6-8L PACKAGE OUTLINE DRAWING



Top View
[顶视图]



Bottom View
[背视图]



Side View
[侧视图]

Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	0.800	1.100	0.031	0.043
A1	0.000	0.05	0.000	0.002
D	-	5.4	-	0.212
E	-	6.250	-	0.246
D1	3.900	4.200	0.153	0.165
E1	3.350	3.650	0.132	0.144
D2	4.800	5.150	0.189	0.203
E2	5.500	5.950	0.216	0.234
k	1.100	1.500	0.043	0.059
b	0.250	0.510	0.010	0.020
e	1.170	1.370		
L	0.510	0.800	0.020	0.031
θ	6°	14°	6°	14°